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# Sensor Technology Integration and Hermetic Module Fabrication Using Liquid Crystal Polymer

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# AGENDA



### 1. Motivation

- Liquid Crystal Polymer (LCP)

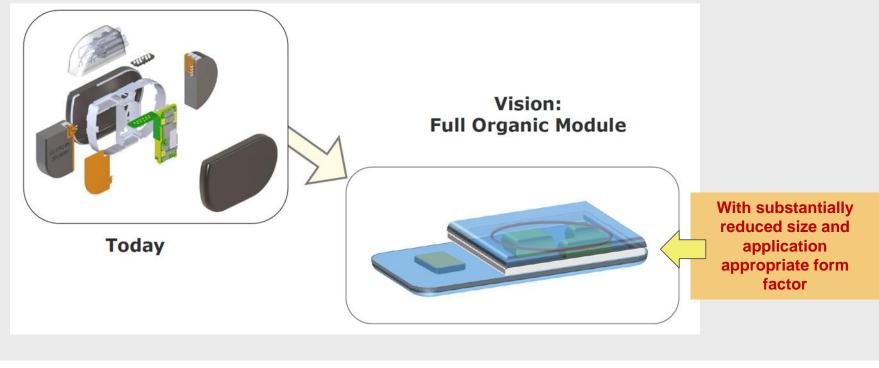
   a Thermoplastic Material for Flexible, Multi-Layer Laminated Substrates
- 3. Encapsulation of Components in LCP Substrates
- 4. Test Results from Temperature Accelerated Soak Testing of Embedded Silicon Dies







## MOTIVATION







## LIQUID CRYSTAL POLYMER (LCP)

#### MATERIAL PROPERTIES

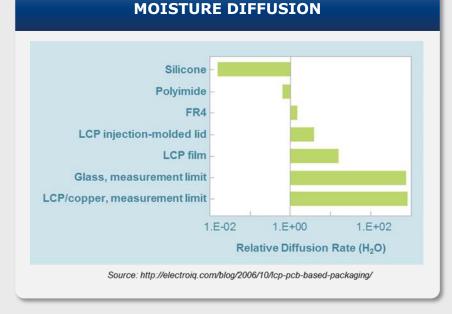
- Flexible thermoplastic base material
- Chemically Inert under most conditions
- Temperature stable up to 190°C (Tg > 280°C, Td > 320°C)
- Very low water absorption (0.04 %) & diffusion rates
- Low weight (1.4 g/cm<sup>3</sup>)
- Excellent high frequency properties  $(\epsilon_R = 2.9, \tan \theta = 0.0025)$
- For multilayer, lamination forms one homogeneous dielectric material







## LCP NEAR-HERMETIC & BIOCOMPATIBLE



Test	Test Duration	No Animals	Result	
USP Systemic Toxicity in the Mouse of Saline Extracts	4, 24, 48 an 72 Hours	10	Pass	
USP Systemic Toxicity in the Mouse of Cottonseed Oil Extracts	4, 24, 48 an 72 Hours	10	Pass	
USP Intracutaneous Toxicity in the Rabbit of Saline Extracts	4, 24, 48 an 72 Hours	\$ 2	Pass	
USP Intracutaneous Toxicity in the Rabbit of Cottonseed Extracts	4, 24, 48 an 72 Hours	2	Pass	

Test	Test Duration	No. Animals	Result
USP Elution Cytotoxicity Study	48 Hours	N/A (L-929 mouse fibroblast cells)	Pass
ISO Muscle Implantation in the Rabbit with Hispathology	12 Weeks	3	Pass
ISO Muscle Implantation in the Rabbit with Hispathology	26 Weeks	4	Pass

Source:

LCP Printed Circuits Board Based Packaging and Microfabrication for Implantable Electronic Devices, Linas Jauniskis, Foster Miller Inc, Meptec 2008





## **PVD PROCESS FOR FLEXIBLE SUBSTRATES**

- PVD process allows use almost any metal and inorganic dielectrics and a few semiconductors
- Large format: 36x36 inch
- High throughput: 10'000 Panels per year
- Up to four different materials can be sputtered in the same run
- Substrate materials include polyimide and LCP flexible substrate
- Double sided sputtering possible
- PVD process fully integrated into flexible substrate process – can be combined with standard copper structures on same surface

#### LARGE FORMAT PVD EQUIPMENT



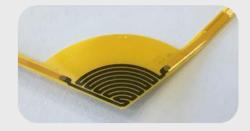


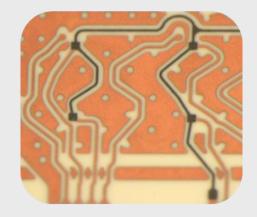


### THIN FILM SENSORS

#### **INTEGRAL SENSORS**

- LCP with non Cu conductors
  - Noble Metal conductors
    - Au, Pt for inert electrodes
    - Pt-Ir fractal surface for low impedance
  - Constantan
    - Thermocouples
    - Temperature invariant Resistors
  - Titanium
    - Thermistors









### **3D SHAPING**

- Thermoplastic polymer properties
  - Define shape
  - Apply Heat
  - Cool Down
- Defined, flexible 3d Shape
- 3d Structures
  - Fit in small spaces
  - Enable stretching



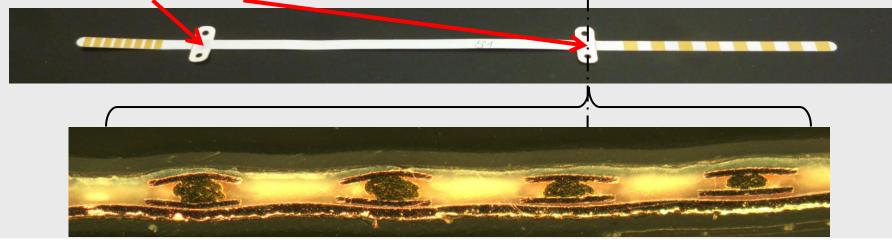






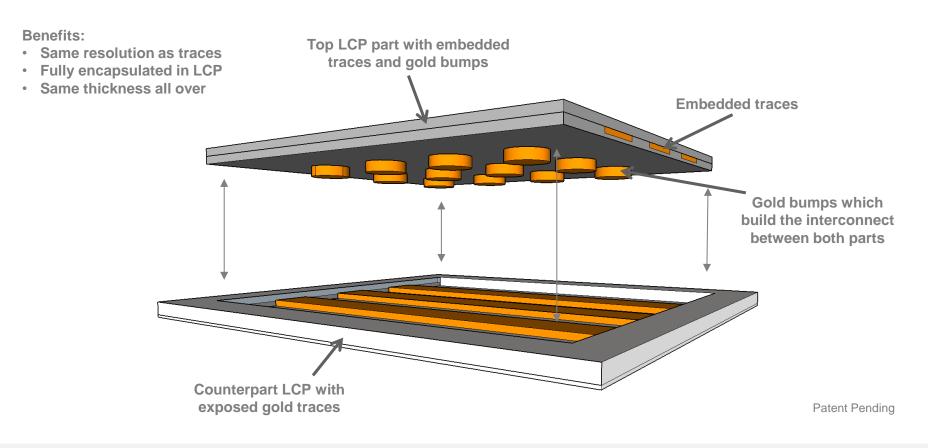
## SEALED INTERCONNECTS BETWEEN CABLES

- Fully encapsulated interconnect technology for LCP flat cables.
- Extension of maximum lead length (> 1 m)
- No solder or adhesive used
- Local applied heat pulse under pressure melts LCP and seals contacts
- Pull strength 20 N/mm same as lead body
- No signal attenuation at interconnect (measured up to 10 GHz)



# **ENCAPSULATED FLAT CABLE INTERCONNECTS**







- Fully encapsulated interconnect technology for LCP flat cables
- Extension of maximum cable length up to more than 1.5 m
- No solder or adhesive used
- Local applied heat pulse under pressure melts LCP and seals contacts
- Pull strength 20 N/mm same as lead body
- No thickness increase at interconnect
- No signal attenuation at interconnect (measured up to 10 GHz)
- Process cycle time for welding: < 5 s

#### LCP FILM WELDING EQUIPMENT

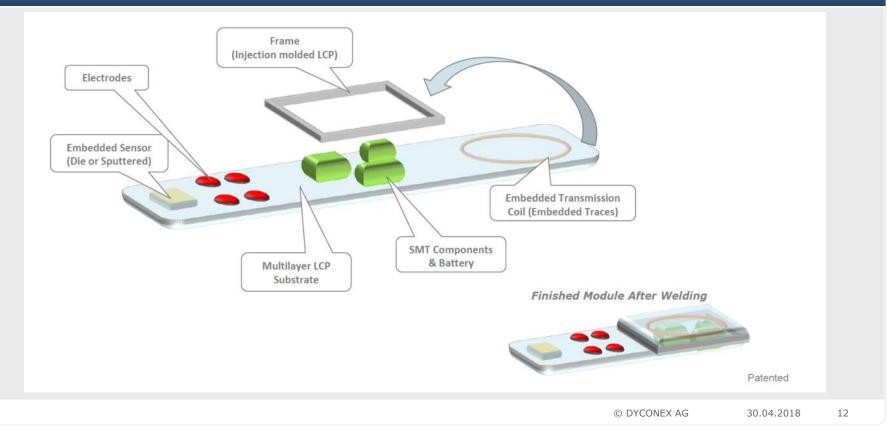


Semi-Automated Welding Machine





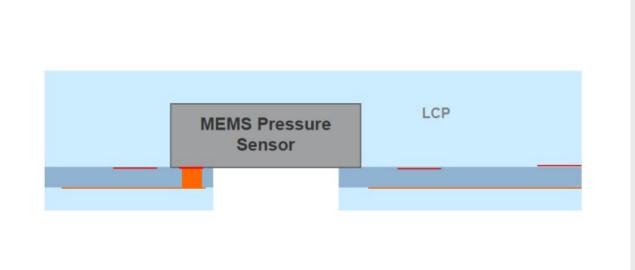
### LCP WELDED MODULE







### **EMBEDDED PRESSURE SENSOR**



Embedding of Pressure Sensor with Opening for Membrane



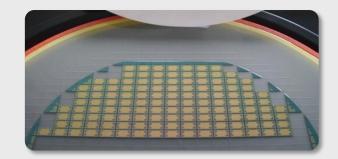
### **TEST CHIP DESIGN**

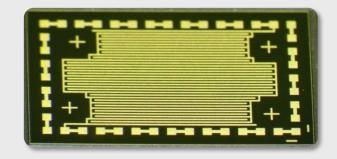
#### Test chip for embedding

- Manufacturer: TLMI, Austin, Texas
- Wafer diameter: 150 mm
- Active wafer diameter: 147 mm
- Wafer thickness: 100 µm (thinned)
- Die surface: thermal oxide
- Adhesion promoter: TiW (sputtered)
- Metallization: 3 µm copper & gold flash
- Chip dimensions: 5 x 8 mm

#### Artwork on die

- Pad size: 250 x 250 µm (thinned)
- Interdigitized comb structure with 50 µm lines & spaces





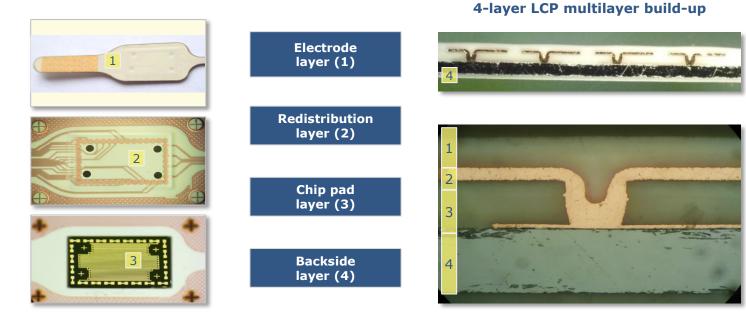


#### FIGURE 2





### **TEST CHIP EMBEDDED IN LCP**







### SOAK TESTING APPARATUS



#### FIGURE 7





## **RECENT SOAK TEST RESULTS**

TEST	LCP STRUCTURE	RESULT
Soak test in PBS at 77° C	LCP with Au traces	<ul> <li>9 months continuous</li> <li>(equivalent to 15 years</li> <li>implanted in body)</li> </ul>
Soak test in PBS at 50° C	LCP with Cu traces, embedded die	Ongoing test (no measureable drop in resistance after >14 months)
Soak test in 40% $H_2SO_4$ at 50° C	LCP with Cu traces, embedded die	Ongoing test (no measureable drop in resistance after >14 months)
Soak test in 80% $H_2SO_4$ at 50° C	LCP with Cu traces, embedded die	Ongoing test (no measureable drop in resistance after >14 months)





## CONCLUSIONS

Flexible Thermoplastic Substrate Technology Tool Box - Benefits:

- □ Low cost polymer material LCP
- $\square$  Thermoplastic substrate material  $\rightarrow$  3D shaping
- Encapsulation with same substrate material possible
  - ightarrow best thermo-mechanical matching and adhesion
  - ightarrow no additional materials, no adhesives needed
- Multilayer construction with thin dielectric layers
  - $\rightarrow$  embedding of traces and components
- □ Additive thin film metallization: Pd, Au, Cu, Ti, TiW, W, etc.
  - $\rightarrow$  special metals/semiconductors for sensor functionality
- □ Module sealing with LCP



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# Thank you for your attention.

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